

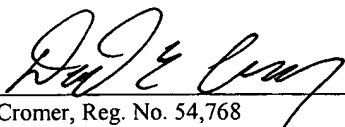


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application)	PATENT APPLICATION
Inventors: Adrian J. Isles)	
Application No.: 09/586,191)	Art Unit: 2123
Filed: June 2, 2000)	Examiner: Sharon, Ayal I.
Title: CIRCUIT-LEVEL MEMORY AND COMBINATIONAL BLOCK MODELING)	Customer No. 28554

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 17, 2005.


David E. Cromer, Reg. No. 54,768
Signature Date: March 17, 2005

RESPONSE C TO OFFICE ACTION UNDER 37 C.F.R. §1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This **RESPONSE C** is in reply to the Office Action mailed December 8, 2004.

AMENDMENTS TO THE CLAIMS are reflected in the listing of claims which begins on page 2 of this RESPONSE.

REMARKS begin on Page 19 of this RESPONSE.